

Acinput Type Photocoupler

LTV814/LTV824/LTV844

LTV814S/LTV824S/LTV844S

LTV814M/LTV824M/LTV844M

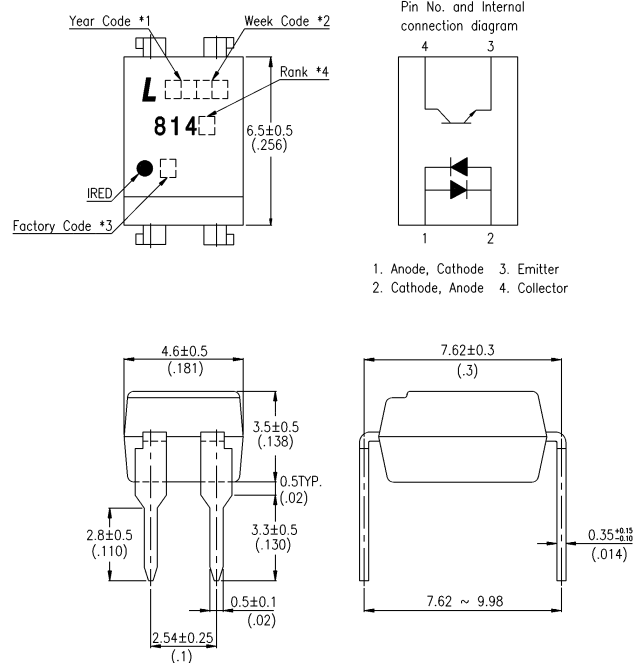
Features

- AC input response
- High input-output isolation voltage :
(Viso : 5,000V_{rms})
- Low collector dark current
(I_{CEO} : MAX. 10⁻⁷A at V_{CE} = 20V)
- Current transfer ratio
(CTR : MIN. 20% at I_F = ± 1mA, V_{CE} = 5V)
- Compact dual-in-line package
LTV814 : 1-channel type
LTV824 : 2-channel type
LTV844 : 4-channel type
- UL approved (No. E113898)
- TUV approved (No. R9653630)
- CSA approved (No. LR91533)
- FIMKO approved (No. 193422)
- NEMKO approved (No. P96103013)
- DEMKO approved (No. 303986)
- SEMKO approved (No. 9646047/01-30)
- VDE approved (No. 94722 Thailand)
- Options Available :
 - Leads with 0.4" (10.16mm) Spacing (M Type)
 - Lead Bends for Surface Mounting (S Type)
 - Tape and Reel of Type I for SMD(Add"-TA"Suffix)
 - Tape and Reel of Type II for SMD(Add"-TA1" Suffix)
 - VDE 0884 Approvals (Add "-V" Suffix)

Applications

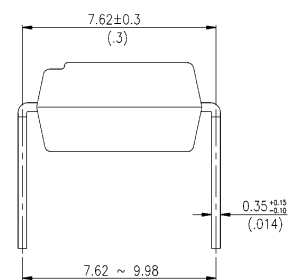
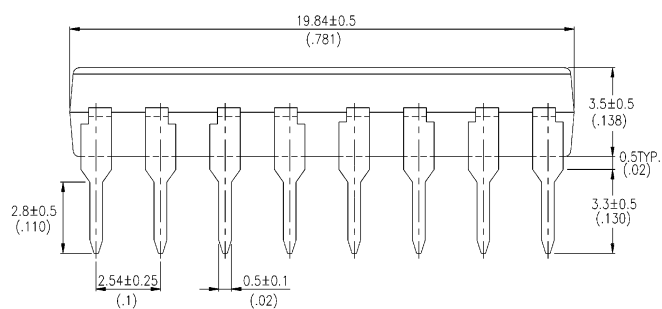
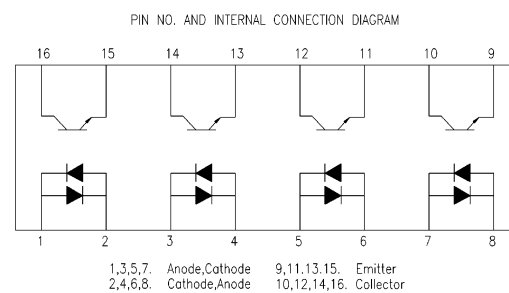
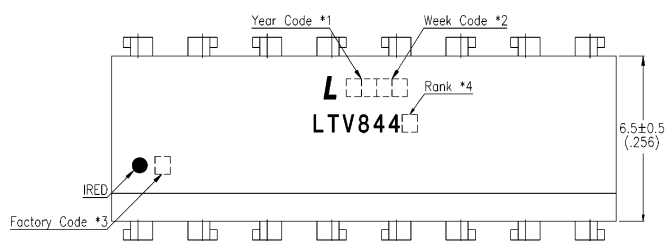
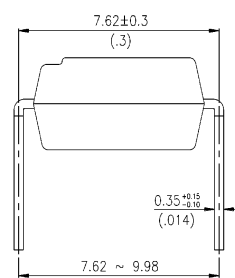
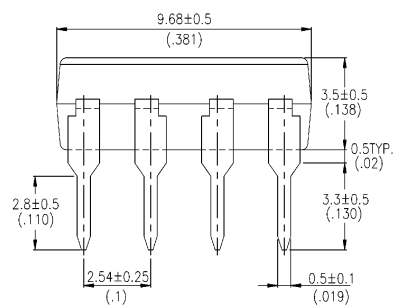
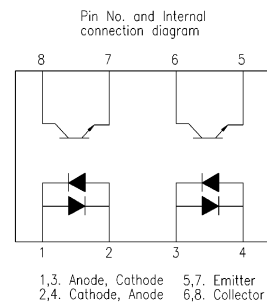
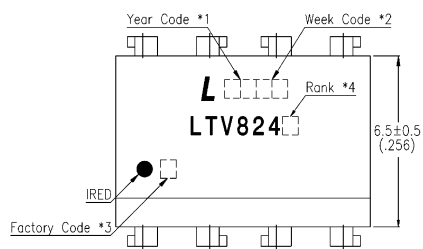
1. Telephone sets, telephone exchangers.
2. Sequence controllers.
3. System appliances, measuring instruments.
4. Signal transmission between circuits of different potentials and impedances.

Package Dimensions



Notes :

1. Year date code.
2. 2-digit work week.
3. Factory code shall be marked (Z : Taiwan, Y : Thailand).
4. All dimensions are in millimeters (inches).
5. Tolerance is ± 0.25mm (.010") unless otherwise noted.
6. Specifications are subject to change without notice.



Absolute Maximum Ratings

(Ta=25 °C)

Parameter		Symbol	Rating	Unit
Input	Forward Current	I _F	± 50	mA
	Power Dissipation	P	70	mW
Output	Collector-Emitter Voltage	V _{CEO}	35	V
	Emitter-Collector Voltage	V _{ECO}	6	V
	Collector Current	I _C	50	mA
	Collector Power Dissipation	P _C	150	mW
Total Power Dissipation		P _{tot}	200	mW
Operating Temperature		T _{opr}	-30~+100	°C
Storage Temperature		T _{stg}	-55~+125	°C
* 1. Isolation Voltage		V _{iso}	5	KV _{rms}
* 2. Soldering Temperature		T _{sol}	260	°C

* 1. AC for 1 minute, 40 ~ 60%R.H.

• Isolation voltage shall be measured using the following method.

(1) Short between anode and cathode on the primary side and between collector, emitter and base on the secondary side.

(2) The isolation voltage tester with zero-cross circuit shall be used.

(3) The waveform of applied voltage shall be a sine wave.

* 2. For 10 seconds.

Electrical/Optical Characteristics

(Ta=25 °C)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Conditions
Input	Forward Voltage	V _F	—	1.2	1.4	V	I _F = ± 20mA
	Terminal Capacitance	C _t	—	50	250	pF	V=0, f=1KHz
Output	Collector Dark Current	I _{CEO}	—	—	100	nA	V _{CE} =20V
	Collector-Emitter Breakdown Voltage	BV _{CEO}	35	—	—	V	I _C =0.1mA
	Emitter-Collector Breakdown Voltage	BV _{ECO}	6	—	—	V	I _E =10μA
Transfer Characteristics	Collector Current	I _C	0.2	—	3	mA	I _F = ± 1mA, V _{CE} =5V
	* Current Transfer Ratio	CTR	20	—	300	%	I _F = ± 1mA, V _{CE} =5V
	Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	0.1	0.2	V	I _F = ± 20mA, V _{CE} =1V
	Isolation Resistance	R _{ISO}	5 × 10 ¹⁰	10 ¹¹	—	Ω	DC500V, 40~60% R.H.
	Floating Capacitance	C _f	—	0.6	1.0	pF	V=0, f=1MHz
	Cut-off Frequency	f _c	15	80	—	KHz	V _{CE} =5V, I _F =2mA R _L =100 Ω, -3dB
	Response Time (Rise)	t _r	—	4	18	μs	V _{CE} =2V, I _F =2mA R _L =100 Ω
	Response Time (Fall)	t _f	—	3	18	μs	

$$* \text{CTR} = \frac{I_C}{I_F} \times 100\%$$

Typical Electrical/Optical Characteristic Curves (25 °C Ambient Temperature Unless Otherwise Noted)

Fig. 1 Forward Current vs. Ambient Temperature

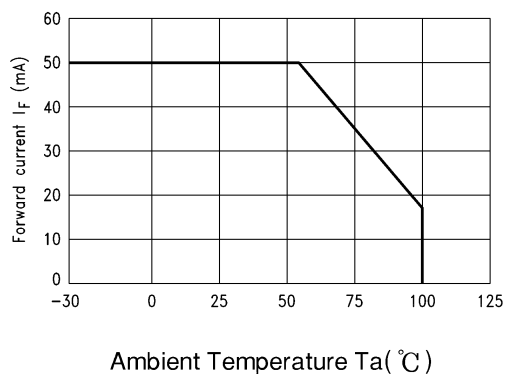


Fig. 2 Collector Power Dissipation Vs. Ambient Temperature

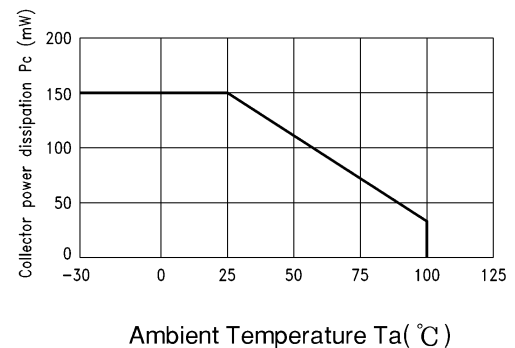


Fig. 3 Collector-emitter Saturation Voltage Vs. Forward Current

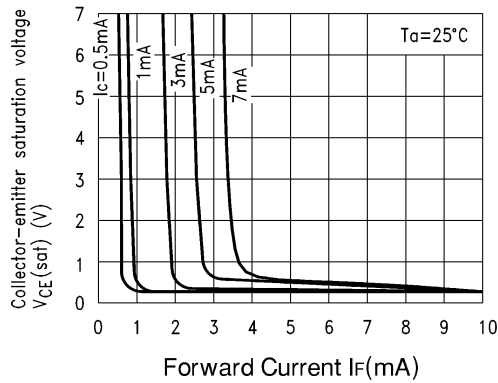


Fig. 4 Forward Current Vs. Forward Voltage

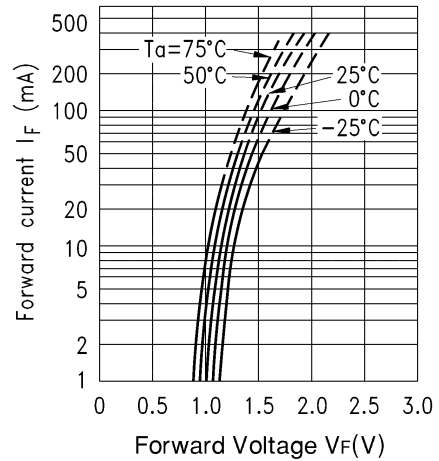


Fig. 5 Current Transfer Ratio Vs. Forward Current

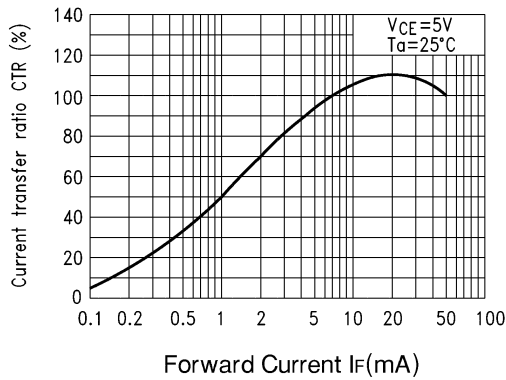


Fig. 6 Collector Current Vs. Collector-emitter Voltage

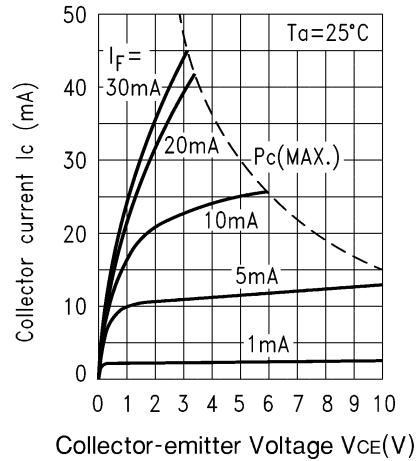


Fig. 7 Relative Current Transfer Ratio Vs. Ambient Temperature

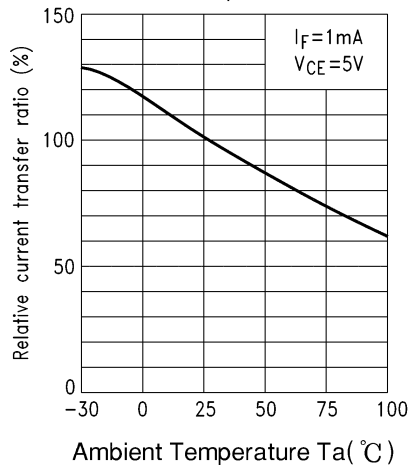


Fig. 8 Collector-emitter Saturation Voltage Vs. Ambient Temperature

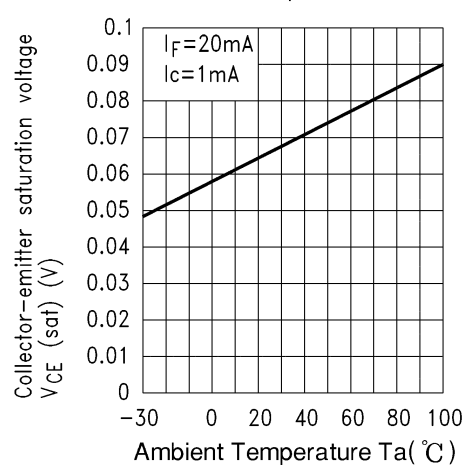


Fig. 9 Collector Dark Current Vs. Ambient Temperature

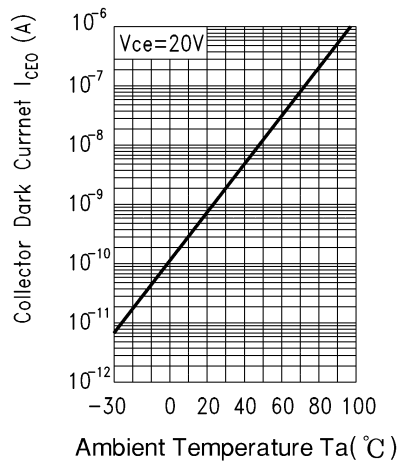


Fig. 10 Response Time Vs. Load Resistance

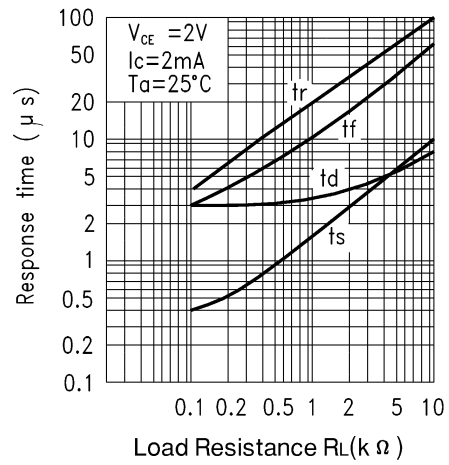
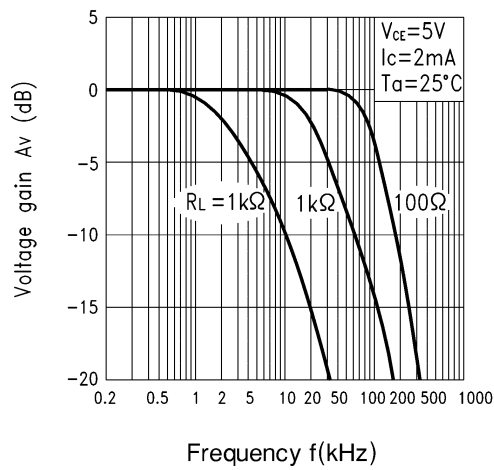
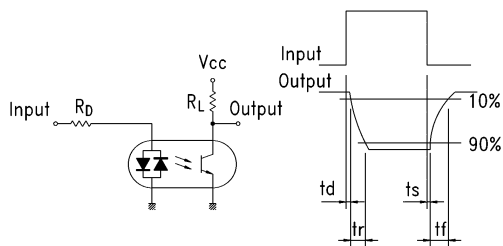


Fig. 11 Frequency Response



Test Circuit For Response Time



Test Circuit For Frequency Response

